

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Duplicate Snoop Tags Partitioned Across Multiple
Processor/Cache Chips in a Multi-Processor System

Application Number :

Confirmation Number:

First Named Applicant: Jack Choquette

Attorney Docket Number: AZ-102

Art Unit:

Examiner:

Search string: (6493797 or 6272602 or 6108752 or 6076147 or 5978886 or 5946709 or 5765196
or 5634068 or 5590310 or 20020174305 or 20020087807 or 20040068616).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6493797	2002-12-10	Lee , et al.	B1	711	118
	2	6272602	2001-08-07	Singhal , et al.	B1	711	144
	3	6108752	2000-08-22	VanDoren , et al.	B1	711	117
	4	6076147	2000-06-13	Lynch , et al.	B1	711	146
	5	5978886	1999-11-02	Moncton , et al.	B1	711	118
	6	5946709	1999-08-31	Arimilli , et al.	B1	711	119
	7	5765196	1998-06-09	Liencrest , et al.	B1	711	143
	8	5634068	1997-05-27	Nishtala , et al.	B1	711	141
	9	5590310	1996-12-31	Willenz , et al.	B1	711	146

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020174305	2002-11-21	Vartti, Kelvin S.	A1	711	145
	2	20020087807	2002-07-04	Gharachorloo, Kourosh ; et al.	A1	711	141
	3	20040068616	2004-04-08	Tierney, Gregory E. ; et al.	A1	711	141

Signature

Examiner Name	Date